This session proposes new approaches to interconnect optimisation. The first paper optimises off-chip busses, while the other two papers improve buffering for off-chip data transmission.

**Friday Workshop W1: MEMS/MST and Their Perspective in Electronic Systems**

**Organisers:** Michael Kraft, Southampton U, UK
*Richard Syms, Imperial College London, UK*

**Description:** MEMS or MST has been a hot topic of research for nearly twenty years now. With the exception of some notable examples it has yet not managed to find applications in a wide range of electronic systems. This is partly due to lack of communication between the MEMS research and the electronic systems design community. Over the last couple of years, MEMS has matured considerably, and there are many examples that have the potential for successful integration for mass-market electronic systems. Therefore, there is a huge potential for mutual benefits and opportunities for both communities. This workshop is intended to contribute to overcome some of the communication barriers. It aims to provide an overview of the diverse field of MEMS including areas such as inertial sensors, simulation and modelling of MEMS, microfluidics, design and test methodologies for MEMS and optical MEMS. The intention of the talks will be to emphasise their potential as an enabling technology that can stimulate the design and development of innovative new products.